

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
Tadashi IGUCHI et al.)	Group Art Unit: 2826
Application No.: 10/612,033)	Examiner: Kevin V. QUINTO
Filed: July 3, 2003)	
For: SEMICONDUCTOR MEMORY)	Confirmation No.: 5852
PREVENTING AN ELECTRIC SHORT)	
CIRCUIT BETWEEN A WORD LINE)	
AND A SEMICONDUCTOR)	
SUBSTRATE, AND MANUFACTURING)	
METHOD FOR THE)	
SEMICONDUCTOR MEMORY)	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

PETITION FOR EXTENSION OF TIME

Applicant petitions for a one month extension of time to file a reply to the Final
Office Action of May 27, 2008. A fee of \$120.00 is enclosed.

Please grant any additional extensions of time required to enter the attached
reply and charge any additional required fees to Deposit Account 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

Dated: September 29, 2008

By: /Jia W. Lu
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